

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Vivek Agarwal; Victor Prince Sundarsingh
Serge Bontemps and Alain Calmels

U.S. Serial No. 10/689,863

Group Art Unit 2838

Filed: October 20, 2003

For: POWER CONVERTER METHOD AND APPARATUS HAVING HIGH
INPUT POWER FACTOR AND LOW HARMONIC DISTORTION

Confirmation No. 3341

Mail Stop Amendment
Commissioner for Patents
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on 28 June 2005

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Supplementing the Information Disclosure Statements filed October 15, 2004 and January 13, 2005, applicant submits herewith PTO Form 1449 and copies of publications of which he is aware which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR Section 1.56.

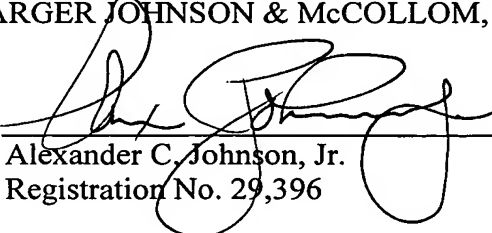
The Andreyck publications are closely related to the publication listed as number [27] in the references enumerated on page 4 of the specification. The exact preliminary publication listed in the application cannot be located, but the Application Note U-132 appears to be a final or revised version of that publication.

The Kamashita reference, no. [23] on page 4 of the specification, and the Micro Linear Data Book 1993, reference [39] on page 6, likewise cannot be found.

As for reference [18] listed on page 4, applicant submits the relevant part, IEC International Standard 1000-3-2, first edition, 1995-03.

20575
Customer No.

Respectfully submitted,
MARGER JOHNSON & McCOLLOM, P.C.

By 
Alexander C. Johnson, Jr.
Registration No. 29,396

1030 S.W. Morrison Street
Portland, Oregon 97205
Telephone: (503) 222-3613

cc: Dah Wen Tsang

Form PTO-1449 (modified)
INFORMATION DISCLOSURE CITATION

Atty. Docket No.
1138-102

Serial No.
10/689,863

Applicant:
Agarwal, et al.

Filing Date
October 20, 2003

Group
2838

Non-Patent Documents

Other Documents (including Author, Title, Source, Date, Pertinent Pages)

____ Andreycak, Bill, "Power Factor Correction Using The UC3852 Controlled On-Time Zero Current Switching Technique, Application Note U-132, Unitrode Corporation, Merrimack, New Hampshire, 1991, pp. 3-235- 3-250.

____ Andreycak, Bill, "Controlled ON-Time, Zero Current Switched Power Factor Correction Technique," Texas Instruments, 2001, pp. 3-1 – 3-11.

____ International Standard, Electromagnetic Compatibility (EMC) - Part 3: Limits – Section 2: Limits for harmonic current emissions (equipment input current ≤ 16 A per phase), CEI/IEC 1000-3-2; 1995, p. 7; 31; 39.

Examiner

Date Considered

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.